



From: todor.toshev@esa.int
Subject: [Sol_sciops_info] [SOLO][MIB] MIB database 27920PFM_CSW3.1.1_MAS7.21 propagated to OPS LAN and MOIS
Date: 5 October 2020 at 12:55
To: sol_fct sol_fct@esa.int
Cc: BS_MCS_DSM@esa.int, BS_MCS_SWS@esa.int, David.Antal-Wokes@esa.int, Petr.Shlyayev@esa.int, Sabine.Kielbassa@esa.int, Thierry.Bernardi@esa.int, solarorbiter_sciops@sciops.esa.int

Hi,

MIB version 27920PFM_CSW3.1.1_MAS7.21 has been built and delivered to OPS LAN. The base SRDB for this MIB is **MAS Version 7.21 and CSW version 3.1.1** from Astrium.

VDF_NAME	VDF_COMMENT	VDF_DOMAINID	VDF_RELEASE	VDF_ISSUE
27920PFM	20201005- CSW3.1.1_MAS7.21	0	27920	721

Outputs and Products:

MOD-FDS file for FD: S:\Solar Orbiter Project Drive\20 Ground\06 Database\10 - MIB\0 - CURRENT MIB\MOD-FDS-Files\ 27920PFM_CSW3.1.1_MAS7.21_MODFDS.txt
ACCESS DB: S:\Solar Orbiter Project Drive\20 Ground\06 Database\10 - MIB\0 - CURRENT MIB\CSW3.0.1_MS Access format\ 27920PFM_CSW3.1.1_MAS7.21
ASCII files: S:\Solar Orbiter Project Drive\20 Ground\06 Database\10 - MIB\0 - CURRENT MIB\CSW3.0.1_ASCII files\ 27920PFM_CSW3.1.1_MAS7.21

Propagated to:
 SOLMCA, SOLMCB, SOLLTA and all their clients
 MOIS
 solmc2, + it's clients

Implemented DCRs:

ID	Created	Title	Requested by
SOL_DCR-518	2020-10-01 13:24	CRP-000 : Handle OBC SpW Cplr Errors	Daniel Lakey
SOL_DCR-517	2020-09-30 13:26	[pending MIB release] EUI Implement Updated IDB version v4.16	Jose-Luis Pellon-Bailon
SOL_DCR-516	2020-09-29 09:05	CRP-000 : update for YIA58831 (SWA_E_HIS_SPW_RX_Q_ERR)	Daniel Lakey
SOL_DCR-515	2020-09-28 21:04	[pending MIB release] Modify OOL set for NRU02722/NRU12722	Alexandros Minogiannis
SOL_DCR-513	2020-09-25 09:49	[pending MIB] NSMD0074 & 75 - set # violations = 2	Daniel Lakey
SOL_DCR-512	2020-09-24 17:37	[pending MIB release + load new MIB on CRP-000 and update shared...]	Ana Mestre
SOL_DCR-511	2020-09-24 17:27	[pending MIB release + load new MIB on CRP-000 and update shared drive]...	Ana Mestre
SOL_DCR-510	2020-09-24 17:15	[pending MIB release + load new MIB on CRP-000 and update shared drive]...	Ana Mestre
SOL_DCR-509	2020-09-24 08:31	[pending MIB release] Implement new PHI database	Sylvain Lodiot

ID	Created	Title	Requested by
SOL_DCR-520	2020-10-01 14:32	STIX Update Whitelist	Jose-Luis Pellon-Bailon F
SOL_DCR-519	2020-10-01 14:16	EUI: Update Whitelist	Jose-Luis Pellon-Bailon F

New/Updated Sequences

CSF_NAME	CSF_DESC	CSF_DESC2	CSF_GENTIME	CSF_DOCNAME	CSF_ISSUE	CSF_DATE
ADMC423A	Reset ObsSpwAHealth v01	SOL_FCR-484	13:06:43	DM-CRP-423		101/10/2020
ADMC423B	Reset ObsSpwBHealth v01	SOL_FCR-484	13:06:43	DM-CRP-423		101/10/2020
ADMC571A	SSMM RdHardSoftErrM1 v01	SOL_FCR-386	13:52:18	DM-CRP-571		130/09/2020
ADMC571B	SSMM RdHardSoftErrM2 v01	SOL_FCR-386	13:52:19	DM-CRP-571		130/09/2020
ADMC571C	SSMM RdHardSoftErrM3 v01	SOL_FCR-386	13:52:19	DM-CRP-571		130/09/2020
AIAF075A	SWA HIS Safe HVs v03	SOL_FCR-472	09:30:52	IA-FCP-075		328/09/2020
AIAF075B	SWA HIS SafeHv4SaMov v03	SOL_FCR-472	09:30:52	IA-FCP-075		328/09/2020
AIPF455A	PHI Set Proc Env v01	SOL_FCR-483	08:28:55	IP-FCP-455		101/10/2020
AIPF563A	PHI Calculate Focus v03	SOL_FCR-483	08:25:19	IP-FCP-563		301/10/2020
AIPF706A	PHI Crop Dataset v01	SOL_FCR-483	08:28:56	IP-FCP-706		101/10/2020
AIUC020A	EUI to Config v01	SOL_FCR-357	09:57:55	IU-CRP-020		102/10/2020
AIUC020B	EUI HTRs Off Config v01	SOL_FCR-357	09:57:55	IU-CRP-020		102/10/2020
AIUC020C	EUI Prep to Safe v01	SOL_FCR-357	09:57:56	IU-CRP-020		102/10/2020
AIUC021A	EUI to Config v01	SOL_FCR-357	09:58:02	IU-CRP-021		102/10/2020
AIUC021B	EUI HTRs Off Config v01	SOL_FCR-357	09:58:02	IU-CRP-021		102/10/2020
AIUC021C	EUI Prep to Safe v01	SOL_FCR-357	09:58:02	IU-CRP-021		102/10/2020
AIUC021D	EUI SWOFF v01	SOL_FCR-357	09:58:03	IU-CRP-021		102/10/2020
AIUUF033A	EUI Wake up v01	SOL_FCR-473	09:49:35	IU-FCP-033		102/10/2020
AIUUF470A	FSI Reg writes v04	SOL_FCR-474	09:45:44	IU-FCP-470		402/10/2020
AIUUF471A	EUV Reg writes v03	SOL_FCR-474	09:45:53	IU-FCP-471		302/10/2020
AIUUF472A	LYA Reg writes v03	SOL_FCR-474	09:46:06	IU-FCP-472		302/10/2020
AIUUF480A	Save Seq. Tables v01	SOL_FCR-473	09:49:36	IU-FCP-480		102/10/2020
AIUUF498A	Erase calib map v01	SOL_FCR-473	09:49:37	IU-FCP-498		102/10/2020
AIUUF499A	Copy calib. Map v01	SOL_FCR-473	09:49:39	IU-FCP-499		102/10/2020
AIUUF500A	Load Calib Map v03	SOL_FCR-481	09:40:27	IU-FCP-500		302/10/2020
AIXF013A	ASW load/execute v05	SOL_FCR-462	10:23:11	IX-FCP-013		502/10/2020
AIXF015A	STIX SuSW Config v01	SOL_FCR-465	10:27:32	IX-FCP-015		102/10/2020
AIXF030A	STIX Safe to Config v05	SOL_FCR-466	10:30:10	IX-FCP-030		502/10/2020
AIXF031A	STIX Config to Safe v05	SOL_FCR-466	10:30:11	IX-FCP-031		502/10/2020
AIXF032A	STIX Safe to Nominal v05	SOL_FCR-466	10:30:13	IX-FCP-032		502/10/2020
AIXF033A	STIX Nom. to Safe v05	SOL_FCR-466	10:30:15	IX-FCP-033		502/10/2020
AIXF034A	STIX Safe to Maint v05	SOL_FCR-466	10:30:18	IX-FCP-034		502/10/2020
AIXF035A	STIX Maint to Safe v05	SOL_FCR-466	10:30:20	IX-FCP-035		502/10/2020
AIXF036A	STIX Conf to Nominal v03	SOL_FCR-466	10:30:22	IX-FCP-036		302/10/2020
AIXF037A	STIX Nominal to Conf v03	SOL_FCR-466	10:30:23	IX-FCP-037		302/10/2020
AIXF038A	STIXA Reset to BOOT v05	SOL_FCR-479	10:08:06	IX-FCP-038		502/10/2020
AIXF038B	STIXB reset to BOOT v05	SOL_FCR-479	10:08:06	IX-FCP-038		502/10/2020
AIXF039A	STIXA Warm reset saf v01	SOL_FCR-462	10:23:17	IX-FCP-039		102/10/2020
AIXF039B	STIXB warm reset saf v01	SOL_FCR-462	10:23:17	IX-FCP-039		102/10/2020
AIXF040A	Disable ASW autostar v01	SOL_FCR-462	10:23:20	IX-FCP-040		102/10/2020
AIXF041A	Enable Mem. Load v01	SOL_FCR-462	10:23:27	IX-FCP-041		102/10/2020
AIXF041B	Disable Mem. Load v01	SOL_FCR-462	10:23:28	IX-FCP-041		102/10/2020
AIXF262A	Mem load/chk MUL=1 v04	SOL_FCR-477	11:04:02	IX-FCP-262		402/10/2020
AIXF262B	Load Memory MUL=1 v04	SOL_FCR-477	11:04:03	IX-FCP-262		402/10/2020
AIXF263A	Mem load/chk MUL=4 v04	SOL_FCR-477	11:04:06	IX-FCP-263		402/10/2020
AIXF263B	Load Memory MUL=4 v04	SOL_FCR-477	11:04:07	IX-FCP-263		402/10/2020
AIXF400A	Enable Calib accum v05	SOL_FCR-466	10:49:53	IX-FCP-400		502/10/2020
AIXF400B	Truncate Calib accum v05	SOL_FCR-466	10:49:53	IX-FCP-400		502/10/2020
AIXF426A	Restart Calibration v01	SOL_FCR-462	10:23:42	IX-FCP-426		102/10/2020
AIXF427A	Enab. Cont. calibra. v01	SOL_FCR-462	10:23:45	IX-FCP-427		102/10/2020
AIXF427B	Disab. Cont. Calib. v01	SOL_FCR-462	10:23:45	IX-FCP-427		102/10/2020
AIXF428A	Cal. Spectrum format v01	SOL_FCR-462	10:23:51	IX-FCP-428		102/10/2020

AIXF429A	Upper Temp correct. v01	SOL_FCR-464	10:21:36	IX-FCP-429	102/10/2020
AIXF429B	ELUT upload v01	SOL_FCR-464	10:21:37	IX-FCP-429	102/10/2020
AIXF429C	Flare Loc.LUT upload v01	SOL_FCR-464	10:21:38	IX-FCP-429	102/10/2020
AIXF614A	Filesystem Check v04	SOL_FCR-466	10:47:36	IX-FCP-614	402/10/2020
AIXF940A	STIX read ASIC temp. v01	SOL_FCR-462	10:24:04	IX-FCP-940	102/10/2020
AIXF941A	OnDemand ADC readout v01	SOL_FCR-462	10:24:09	IX-FCP-941	102/10/2020
AIXF942A	Read ASIC baseline v01	SOL_FCR-462	10:24:15	IX-FCP-942	102/10/2020
AIXF943A	ASIC dark cur. read v01	SOL_FCR-462	10:24:23	IX-FCP-943	102/10/2020
AIXF944A	ASIC Threshold scan v01	SOL_FCR-462	10:24:27	IX-FCP-944	102/10/2020
AIXF945A	ASIC ADC read v01	SOL_FCR-462	10:24:32	IX-FCP-945	102/10/2020
AIXF946A	Generate test pulses v01	SOL_FCR-462	10:24:37	IX-FCP-946	102/10/2020
AIXF948A	ASIC register write v01	SOL_FCR-462	10:24:43	IX-FCP-948	102/10/2020
AIXF950A	Det. quarters PWON v01	SOL_FCR-462	10:24:45	IX-FCP-950	102/10/2020
AIXF951A	HVPS PWON v01	SOL_FCR-462	10:24:51	IX-FCP-951	102/10/2020
AIXF952A	SAFE to nominal obs v01	SOL_FCR-462	10:24:57	IX-FCP-952	102/10/2020
ASYF010A	Set TM Bit Rate v08	SOL_FCR-384	14:37:45	SY-FCP-010	829/09/2020
ASYF013A	Configure Start DDOR v03	SOL_FCR-471	07:18:23	SY-FCP-013	328/09/2020
ASYF013B	Configure End DDOR v03	SOL_FCR-471	07:18:24	SY-FCP-013	328/09/2020
ATTF101A	Set DST A Power v10	SOL_FCR-384	14:37:48	TT-FCP-101	1029/09/2020
ATTF101B	Set Power DST B v10	SOL_FCR-384	14:37:48	TT-FCP-101	1029/09/2020

Mit freundlichen Grüßen / Kind Regards,

 Todor Toshev
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